Abstract

A memory device has a memory array for storing memory data, a conditioning data storage unit for storing conditioning data, and data lines for transferring data. During a memory operation, the memory device transfers both of the condition data and the memory data to the same data lines at different time intervals. The condition data is transferred at one time interval. The memory data is transferred at another time interval. Transferring the conditioning data to the data lines improves the accuracy of the transfer of the memory data at the data lines.

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